Attorney Docket No.: 015114-066900US

Client Ref. No.: A1031 CIP

DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Title of Invention		CONTROL CIRCUIT FOR SELF-COMPENSATING DELAY CHAIN FOR MULTIPLE-DATE-RATE INTERFACES				
As the below named inventor(s), I/we declare that:						
This declaration is directed to:						
	\boxtimes	The attached application, or				
		Application No. , filed on ,				
		as amended on (if applic	able);			
I/we believe that I/we am/are the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought;						
I/we have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;						
I/we acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me/us to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.						
All statements made herein of my/our own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.						
FULL NAME OF INVENTOR(S)						
Inventor 1 Yan C	hong		Date:	12/24/03		
Signature:	Har	they	Citizen of:	Peoples Republic of China		
Inventor 2 Chiak	ang Sun	g	Date:	12/24/03	-	
Signature:	inha	of Sury	Citizen of:	United States		
Inventor 3 Josep	h Huang		Date:	12/23/03	-	
Signature:	anh	Theang	Citizen of:	United States		
Inventor 4 Philip	Pan		Date:	12/30/2003	-	
Signature:	Hul	y, fan	Citizen of:	United States		
Additional invertors are being named on additional form(s) attached hereto.						